

G.F. 64

(भारत सरकार) GOVERNMENT OF INDIA
(रेल मंत्रालय) MINISTRY OF RAILWAYS
(रेलवे बोर्ड) RAILWAY BOARD

No. 2018/Sig/36-SD-1

New Delhi, dated 02.08.2024

**PCSTEs,
All Zonal Railways,
Metro Railway, Kolkata
& CORE/ALD**

Sub: Standardization of Typical Circuits for Electronic Interlocking (EI)
Ver. 3.0


Ref: RB's letter even no. dtd 26.08.2022

Vide letter under reference, Standardized Typical Circuits for Electronic Interlocking (EI) Ver. 2.0 was circulated for implementation in all future EI installations of up to 100 routes.

In continuation of this, Standardized Typical Circuits for Electronic Interlocking (EI) Ver. 3.0 for Automatic Signalling has been finalized & updated by the standing committee vide report no. SS/155/2019 ver 3.0 dtd. 08.07.2024, which is available at RDSO website:

http://10.100.2.19/01_english/signal/policy/uniform_circuit_diagram.htm

Zonal Railways are advised that EI/OC based Automatic Signalling shall be based on these standard typical circuits.


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(रेल मंत्रालय) MINISTRY OF RAILWAYS
(रेलवे बोर्ड) RAILWAY BOARD

No.2018/Sig/36-SD-1

New Delhi, dated 26 .08.2022

PCSTEs
Zonal Railways
Metro Railway, Kolkata
& CORE/ALD

Sub:- Standardization of Typical Circuits for Electronic Interlocking (EI)
Ver. 2.0.

Ref:- RB's Letter of even no. dtd. 26.11.2019.

Vide reference above, Zonal Railways were directed to implement Signalling Standardization scheme and submit feedback. Based on the feedback from various Zonal Railways, their experiences & problems faced during & after implementation of Standard circuits, Standing Committee for standardization of typical circuits for EI was nominated to mitigate the problems as improvement in Typical circuits is a continuous process.

The Committee has finalised its report and updated existing booklet of Standard EI Circuits vide report no. "Standardization of the Typical Circuit for EI"- SS/155/2019 version 2.0 dtd. 29.03.22 available at RDSO website http://10.100.2.19/signal/policy/uniform_circuit_diagram.htm

Zonal Railways are advised that in all future EI installations upto 100 routes Ver 2.0 of Standard circuits must be used.

(Rameshwer Meena)
Executive Director (Signal)

(भारत सरकार) GOVERNMENT OF INDIA
(रेल मंत्रालय) MINISTRY OF RAILWAYS
(रेलवे बोर्ड) RAILWAY BOARD

No. 2018/Sig/36-SD/1

New Delhi, dated 26.11.2019

PCSTEs
Zonal Railways
Metro Railway, Kolkata
& CORE/ALD

Sub:- Standardization of Signaling Drawings

- Ref:-** 1. CSTE/Planning's Conference dt. 30.10.18- Mumbai/WR (Minutes 2018/Sig/36-SD/1 dated 14.11.19)
2. PCSTEs Conference dt. 27.04.2019- Maligaon/NFR (Minutes 2018/Sig/25-Conf/2 dt. 04.06.2019)
3. RDSO's letter No.STS/E/Signaling Principles Vol-II/64 dt.18.10.19

1. Two under mentioned committees were nominated during CSTE/Planning's Conference in October, 2018 at Mumbai, WR over standardization of signalling circuits:

1.1 Standardization of Typical Circuits (Minutes item no 38 of Ref 1)

- | | | |
|-----------------------|---------------------------------|-------------|
| a) Shri S. Khandelwal | CSTE/C/ECR, presently ED/T/RDSO | - Convener |
| b) Shri V.K. Pandey | Dy.CSTE(Plg.)/NR | - Member |
| c) Shri Siddhu Yadav | Dy.CSTE/P&D/NCR | - Member |
| d) Shri M M Waris | Jt.Dir/Sig-III/RDSO | - Secretary |

The Committee has discussed the draft report during PCSTEs' conference in April, 2019 at Maligaon, NFRailway (Ref 2) and further with EI OEMs in July, 2019. The Committee has since submitted its report on 'Standardization of Typical Circuits' for Electronic Interlocking upto 100 routes to Railway Board (Ref 3). The report is available at RDSO website http://10.100.2.19/signal/policy/uniform_circuit_diagram.htm.

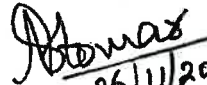
1.2 Standardization of Signalling Drawings (Minutes item IV of Ref 1)

- | | | |
|--------------------------|---------------|-------------|
| a) Shri P Venkata Ramana | Dean/IRISET | - Convener |
| b) Shri K V.Reddy | CSTE/Plg./SCR | - Secretary |

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- | | | |
|-------------------------|---------------------|----------|
| c) Shri K N Kherotia | CSE/WR | - Member |
| d) Shri Benu Halder | Dy. CSTE/P&D/SER | - Member |
| e) Shri K Kameshwar Rao | DSTE/Const./Drg./SR | - Member |

The committee has discussed the report during PCSTEs' conference in April, 2019 at Maligaon, NFRailway (Ref 2) and further conducted a workshop in June, 2019 at IRISSET with Zonal Railways. The committee has since finalized the report and it is available at IRISSET website <http://10.195.2.19/iriweb/wiki/LearningResources>.

2. Zonal Railways are directed to implement the standardization schemes of the above two reports. It is also directed that at least one station may be completed by 31.03.2020 and submit feedback.


26/11/2019
(Arjun Singh Tomar)
निदेशक (सिगनल)

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