

भारत सरकार **GOVERNMENT OF INDIA**
रेल मंत्रालय **MINISTRY OF RAILWAYS**
रेलवे बोर्ड **(RAILWAY BOARD)**

No.2024/Sig/25 Conf./89th SSC Mtg.

New Delhi, Dt. 13.03.2024

**General Managers,
All Zonal Railways**

Sub: Approval of HQ Level Circuit diagrams for EI works.

Ref: (i) RDSO letter no.STS/C/89th SSC Meeting dtd 12.03.2024
(ii) Rly Bds letter no.2018/Sig/7/6-RVNL/1 dtd. 09.02.2022

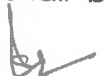
The issue of capacity constraints being faced by Zonal Railways in expeditious checking and approvals of Signal Design circuits / logic and resultant bottleneck in timely commissioning infrastructure works has been brought out by Zonal Railways during reviews.

At present IRSEM enunciates HQ level drawings checking in full at two levels by officers and approval thereafter by Dy.CSTE or above as contained in Para 8.1.6 (c) Table 2 of IRSEM. Zonal Railways are raising the issue of non-availability of JS/SS officers and manifold increase in targets resulting in difficulty in fulfilling of these provisions.

Earlier, vide Railway Board's letter dtd. 09.02.2022 (ref (ii) above), PCSTEs/CAOs were authorized to enter into contracts for independent checking and verification of application logic and interface circuits for EI works before final checking / approval by Railways.

In light of constraints being faced by Zonal Railways, the issue has been deliberated with all zonal railways through Signalling Standard Committee on 07.03.2024 and it is decided that:

- 1) PCSTEs of the Zonal Railways based on their requirements, may authorize the 1st level of checking (as mandated in IRSEM) to competent SSE(Signal)/SSE(D&D), on a trial basis for a period of 1 year, subject to the following:
 - a. PCSTE is satisfied about the level of competency before authorizing SSE(Signal)/SSE(D&D) for 1st level checking. For competency assessment, PCSTE may devise a mechanism as deemed appropriate.
 - b. Standard circuits are adopted with routes upto 100.
 - c. SSE(Signal)/SSE(D&D) who is authorized to do 1st Level check, shall be different from SSE who has drawn the circuits.




2) It is also decided that the 2nd Level checking of the HQ level drawings can be done by SSTE(D&D)/ASTE(D&D) before approval.

Based on the above, PCSTEs shall submit a feedback after a period of 6-8 months on the basis of the experience gained, for further necessary action in this regard.

It may be ensured that all other safety checks are carried out and properly documented as per extant guidelines.

This issues with the approval of Member (Infra).



13.03.2024

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