

(भारत सरकार) GOVERNMENT OF INDIA  
(रेल मंत्रालय) MINISTRY OF RAILWAYS  
(रेलवे बोर्ड) RAILWAY BOARD

(67) 9 B

No.2018/Sig/7/6-RVNL/1

New Delhi, 18<sup>th</sup> February, 2019

**Chairman & Managing Director**  
RVNL  
B Block, 1<sup>st</sup> Floor, August Kranti Bhawan  
Bhikaji Cama Place  
New Delhi - 110066

Sub: - Empowering RVNL for Approval of Design documents of S&T installation under RVNL projects.

Ref: - (i) RVNL's letter No. RVNL/S&T/IB/Rly. Bd. dtd. 8<sup>th</sup> Jan. 2019  
(ii) This office letter of even No. dated 07.02.2019

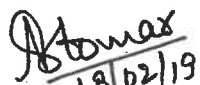
In supersession to Board's letter of even no dated 07.02.2019 under ref (ii), the following is issued for empowering RVNL for approval of design documents of S&T installation under RVNL projects.

The proposal for allowing RVNL to approve Logic and Interface circuits using IRSE/IRSTELO License holders while keeping approval of Signal and Interlocking Plan (SIP) and Table of Control (TOC) with the Railways has been examined.

RVNL is allowed on a trial basis to approve Logic and Interface circuits for stations upto 50 routes for the Doubling and Electrification work in Daund - Gulbarga section subject to:-

- i) The approval of SIP & TOC will continue to be with Railway.
- ii) IRSE/IRSTELO license holders to be empanelled by RVNL who only will be authorized to approve the Logic and Interface circuits.
- iii) Factory Acceptance Test (FAT) to be done by Zonal Railway/Drawing Office officials along with RVNL officials at firm's premise or at Zonal Railways' own FAT set up, if available.
- iv) Site Acceptance Test (SAT) to be done by a different set of officials from Zonal Railways along with RVNL officials in the field.
- v) RVNL to adopt the practice as desired by the Zonal Railway till adoption of Standard Typical Circuits are agreed by the Zonal Railways.
- vi) IRSEM Para 8.7.3 & 8.7.4 (ii) to be implemented by Zonal Railway by countersigning the circuits based on approval of circuits by IRSE/IRSTELO License holders empanelled by RVNL.

This has the approval of DG(S&T).

  
18/02/19  
(Arjun Singh Tomar)  
निदेशक (सिगनल)

Copy to: -

PCSTE/CR & SCR - for information and necessary action.