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GOVERNMENT OF INDIA (भारत सरकार)  
MINISTRY OF RAILWAYS (रेल मंत्रालय)  
RAILWAY BOARD (रेलवे बोर्ड)

No. 2018/Sig/7/6-RVNL/1

New Delhi, Dated 28.10.2019

Chairman & Managing Director  
Rail Vikas Nigam Ltd.,  
August Kranti Bhawan, Bhikaji Cama Place,  
R.K. Puram, New Delhi – 110 066

**Sub:-** Empowering RVNL for Approval of Design documents of S&T installation in BZA-GDV-BVRM-NDD-NS-MTM Doubling, UR-MBNR and MMTS Phase II Doubling Project on South Central Railway.

**Ref:-** PED/RVNL's letter No. RVNL/S&T/1B/Rly.Bd dt. 18.09.2019

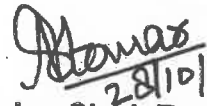
Vide letter under reference above, it has been requested to allow RVNL for approving Interface and Logic Circuit for installation upto 50 routes for Doubling project of BZA-GDV-BVRM-NDD-GDV-MTM-BVRM-NS-MTM (18 stations), Umdanagar – Mahboobnagar (7 stations) and MMTS Phase II (7 stations) on South Central Railway.

The proposal has been examined. RVNL is allowed to approve Logic and Interface circuits for stations upto 50 routes in sections mentioned in para above, subject to:-

(REC) (SLCL) (TOE)

- i) The approval of SIP & TOC will continue to be with Railway.
- ii) IRSE/IRSTELO license holders to be empanelled by RVNL who only will be authorized to approve the Logic and Interface circuits.
- iii) Factory Acceptance Test (FAT) to be done by Zonal Railway/Drawing Office officials along with RVNL officials at firm's premise or at Zonal Railways' own FAT set up, if available.
- iv) Site Acceptance Test (SAT) to be done by a different set of officials from Zonal Railways along with RVNL officials in the field.
- v) RVNL to adopt the practice as desired by the Zonal Railway till adoption of Standard Typical Circuits are agreed by the Zonal Railways.
- vi) IRSEM Para 8.7.3 & 8.7.4(ii) to be implemented by Zonal Railway by countersigning the circuits based on approval of circuits by IRSE/IRSTELO License holders empanelled by RVNL.

This has the approval of MST.

  
28/10/19  
(Arjun Singh Tomar)  
निदेशक (सिगनल)

Copy to – PCSTE/SCR – for information and necessary action.