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**GOVERNMENT OF INDIA
MINISTRY OF RAILWAYS
(RAILWAY BOARD)**

NO. 2018/SIG/36-SD/1

DT. 20.11.2019

**The Director General
RDSO, Manak Nagar, Lucknow.**

Sub:- Standardization of Typical Circuits for Electronic Interlocking.

Ref:- ED(Telecom)/RDSO's letter No. STS/E/Signalling Principles Vol-II/64 dated 18.10.2019

A Committee of under mentioned officers was nominated in October, 2018 for Standardization of Signalling Drawings specially Typical Circuits of Electronic Interlocking.

(I)	Shri S Khandelwal	CSTE/C/ECR now ED/Telecom/RDSO	Convener
(ii)	Shri V K Pandey	Dy.CSTE(Plg)/NR	Member
(iii)	Shri Siddhu Yadav	Dy. CSTE/P&D/NCR	Member
(iv)	Shri M M Waris	Jt.Dir./Sig-III/RDSO	Secretary

- The Committee after extensive consultations with the Railways along with deliberations with manufacturers of OEMs of various Interlockings has submitted its Report on Standardization of Typical Circuits for Electronic Interlocking up to 100 routes.
- The Committee has finalized the Typical Logic for Electronic Interlocking which shall be very useful for having uniform standard circuits for Electronic Interlockings across Indian Railways.
- The Committee Members under the convenership of Shri Shailesh Khandelwal, ED/Telecom/RDSO, have done an excellent job for Standardization of Circuits for Electronic Interlocking and in appreciation of their sincere efforts, a Group Cash Award of Rs.20,000 is sanctioned.
- The Report submitted by the Committee, has been sent to Zonal Railways for implementation.


(Pradeep Kumar)

**Member /Signal & Telecom
Railway Board/New Delhi.**

- Copy to:-** All Committee Members:
- (i) Shri S. Khandelwal, /ED/Tele/RDSO/LKO
 - (ii) Shri V K Pandey, Dy.CSTE(Plg)/NR
 - (iii) Shri Siddhu Yadav, Dy. CSTE/P&D/NCR
 - (iv) Shri M M Waris, Jt.Dir./Sig-III/RDSO